



# Intel® Desktop Board D510MO Specification Update

October 2011

Part Number: E83697-007

The Intel® Desktop Board D510MO may contain design defects or errors known as errata, which may cause the product to deviate from published specifications. Current characterized errata are documented in this Specification Update.

# Revision History

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Revision	Revision History	Date
-001	This document is the first Specification Update for the Intel® Desktop Board D510MO	December 2009
-002	Updated the General Information Section	April 2010
-003	Updated the General Information Section	May 2010
-004	Updated the General Information Section	July 2010
-005	Updated the General Information Section	November 2010
-006	Updated the Specification Changes Section	April 2011
-007	Updated the General Information Section	October 2011

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The Intel® Desktop Board D510MO may contain design defects or errors known as errata which may cause the product to deviate from published specifications. Current characterized errata are available on request.

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# Specification Update for the Intel® Desktop Board D510MO

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This document is an update to the specifications contained in the *Intel® Desktop Board D510MO Technical Product Specification* (Order Number: E74523). It is intended for hardware system manufacturers and software developers of applications, operating systems, or tools. It will contain Specification Changes, Errata, Specification Clarifications, and Documentation Changes.

Refer to the *Intel® Atom Processor D500 Series Specification Update* (Order Number: 434725) for specification updates concerning the Intel Atom D500 series processors that may apply to the desktop board D510MO. Unless otherwise noted in this document, it should be assumed that any Processor errata for a given stepping are applicable to the Altered Assembly (AA) revision(s) associated with that stepping.

Refer to the *Intel® NM10 Express Chipset Specification Update* (Order Number: 417137) for specification updates concerning the Intel® NM10 Express Chipset that may apply to the desktop board D510MO. Unless otherwise noted in this document, it should be assumed that any Platform Controller Hub errata for a given stepping are applicable to the Altered Assembly (AA) revision(s) associated with that stepping.

# Terminology

**Specification Changes** are modifications to the current published specifications. These changes will be incorporated in the next release of the specifications.

**Errata** are design defects or errors. Characterized errata may cause the desktop board behavior to deviate from published specifications. Hardware and software designed to be used with any given Altered Assembly (AA) and BIOS revision level must assume that all errata documented for that AA and BIOS revision level are present on all desktop boards.

**Specification Clarifications** describe a specification in greater detail or further highlight a specification's impact to a complex design situation. These clarifications will be incorporated in the next release of the specifications.

**Documentation Changes** include typos, errors, or omissions from the current published specifications. These changes will be incorporated in the next release of the specifications.

## General Information

**Basic Desktop Board D510MO Identification Information**

AA Revision	BIOS Revision	Notes
E76523-302	MOPNV10J.86A.0115	1,2
E76523-400	MOPNV10J.86A.0148	1,2
E76523-401	MOPNV10J.86A.0154	1,2
E76523-402	MOPNV10J.86A.0175	1,2
E76523-403	MOPNV10J.86A.0175	1,2
E76523-404	MOPNV10J.86A.0400	1,2
E76525-301	MOPNV10J.86A.0115	1,2
E76525-400	MOPNV10J.86A.0148	1,2
E76525-401	MOPNV10J.86A.0154	1,2
E76525-402	MOPNV10J.86A.0175	1,2
E76525-403	MOPNV10J.86A.0175	1,2
E76525-404	MOPNV10J.86A.0400	1,2
E67982-303	MOPNV10J.86A.0115	1,2
E67982-400	MOPNV10J.86A.0148	1,2
E67982-401	MOPNV10J.86A.0154	1,2
E67982-402	MOPNV10J.86A.0175	1,2
E67982-403	MOPNV10J.86A.0175	1,2
E67982-404	MOPNV10J.86A.0400	1,2

Notes:

1. The AA number is found on a small label on the component side of the board.
2. The Intel® CG82NM10 Express Chipset used on this AA revision consists of the following component:

Device	Stepping	S-Spec Numbers
CG82NM10	B0	SLGXX

## Summary of Changes

The following table indicates the Specification Changes, Errata, Specification Clarifications, or Documentation Changes that apply to the Intel® Desktop Board D510MO. Intel intends to fix some of the errata in a future revision of the desktop board, and to account for the other outstanding issues through documentation or specification changes as noted.

The following notations are used in the table:

Doc:	Document change or update that will be implemented.
Plan Fix:	This erratum may be fixed in a future revision of the desktop board, driver, or BIOS.
Fixed:	This erratum has been previously fixed.
No Fix:	There are no plans to fix this erratum.
Shaded:	This erratum is either new or modified from the previous version of the document.

No.	Plans	Documentation Changes
1	Doc	Updated the drawing in Section 2.2.2 Component-side Connectors and Headers to show Item H reverses pin 1 and pin 7.
No.	Plans	Errata
		There are no characterized erratum for this product.

## Specification Changes

The Specification Changes listed in this section apply to the *Intel® Desktop Board D510MO Technical Product Specification* (Order Number E74523).

- Updated the drawing in Section 2.2.2 Component-side Connectors and Headers to show Item H reverses pin 1 and pin 7.

